

Suggested Schedule for incoming transfer EE students

Note -- This schedule applies only for transfer students who have completed a program with a Direct Transfer Agreement (DTA), and the following:

- Calculus I, II, III – MATH 1234, 1235, 1236
- Physics for Science/Engineering I, II, III - PHYS 1121, 1122, 1123
- Differential Equations – MATH 3237
- Linear Algebra – MATH 2401
- Vector Calculus – MATH 3238
- Intro to Programming (C/C++ preferred) AND Data Structures - CSC 1230, CSC 2430 [Note that CSC 1230 and 2430 should be taken at the same institution.]
- Intro to chemistry or high school chemistry

[For coherence with subsequent courses, electrical and computer engineering majors should take Circuits I (EE 2726) at SPU. Students pursuing an electrical or computer engineering degree should begin at SPU in the Fall.]

Year 1

Fall	Winter	Spring
EE 2726 circuits I (4)	EE 2727 circuits II (4)	EE 3028 Circuits III (4)
EE 1210 Logic Systems (5)	EE 3760 Computer Org (5)	EE 3280 Microcontroller (5)
Tech Elective or UFDN (3-5)*	EGR 3310 Electromag. (3)	EGR 2200 Prob&Stats (2)
	Tech Elective or UFDN (3-5) *	Tech Elective or UFDN (3-5) *

Year 2

Fall	Winter	Spring
EE 3721 Electronics I (5)	EE 3722 Electronics II (5)	EE 3730 Electronics III (5)
EE 3550 Communic. (5)	EE 3410 Signals and Sys. (5)	Tech Elective (5)
EE 3000 Junior Seminar (1)	Tech Elective or UFDN (3-5) *	Tech Elective or UFDN (3-5) *
Tech Elective or UFDN (3-5) *		

Year 3**

Fall	Winter	Spring
EE 4211 Senior Design I (3)	EE 4212 Senior Design II (3)	EE 4899 Sen. Design III (3)
EGR 4940 Internship (1)		

* Tech electives are generally available in 3, 4 and 5 credits, with 5 credits the most common. Most have pre-requisites which must be met first. Twelve tech elective credits will be required for graduation.

** Often students will begin working almost full time in an engineering related job once they have completed everything except the senior design sequence.